

Customer No.: 31561
Application No.: 10/064,916
Docket No.: 7558-US-PA

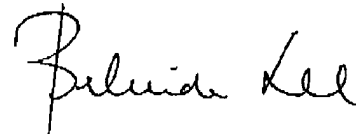
REMARKS

Applicants would like to elect Group I, claims 1-18, drawn to a multi-memory architecture having a memory mapping implementation between the devices and a pin-out not less than the number of pins of one of the memory devices, classified in class 711, subclass 103 and 202. Please cancel Group II, claims 19-26, drawn to a memory access controller, classified in class 711, subclass 154 without waiver, prejudice or disclaimer.

If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

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Respectfully submitted,



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